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PATENT

JUN 1 0 2005

S/N 09/943,134

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Leonard Forbes et al.

Examiner: Tu-Tu V. Ho

Serial No.:

09/943,134

Group Art Unit: 2818

Filed:

August 30, 2001

Title:

Docket: 1303.020US1 PROGRAMMABLE ARRAY LOGIC OR MEMORY DEVICES WITH

ASYMMETRICAL TUNNEL BARRIERS

AMENDMENT UNDER 37 C.F.R. § 1.312

Please Enter

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In response to the Notice of Allowance and Notice of Allowability mailed April 7, 2005, please amend as follows:

JUN 1 0 2005

S/N 09/943,134

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## COMMUNICATION CONCERNING RELATED APPLICATION(S)

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Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

Serial/Patent No.

Filing Date

Attorney Docket

11/062543

February 22, 2005

1303.028US2

SRAM CELLS WITH REPRESSED

FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY

INSULATORS

11/063825

February 23, 2005

1303.132US1

GERMANIUM-SILICON-CARBIDE FLOATING GATES IN MEMORIES

Continuations and divisionals may be later filed on the cases listed above, or cired to the Examiner in any previous Communication Concerning Related Applications. Applicant requests that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

> Respectfully submitted. LEONARD FORBES ET AL.

By Applicants' Representatives,

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Date 10 June 05

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I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown

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